

# Wideband PA Design: The “Continuous” Mode of Operation

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**Abstract** — The introduction and formulation of the “Continuous” modes of transistor operation has provided an alternative design route for the realization of high efficiency power amplifiers (PA’s) over a wide bandwidth (approaching an octave). The formulation of the “Continuous” modes provides a mathematical framework for extending the fundamental/harmonic impedance space over which the power and efficiency performance of the traditional PA modes, i.e. Class, B, Class F, etc, can be maintained. This mathematical framework has been experimentally validated using RF I-V waveform engineering measurements systems and the realization of demonstrator PA prototypes.

**Index Terms** — Microwave measurements, microwave theory and techniques, power amplifiers, power transistors, wideband.

## I. INTRODUCTION

The overall efficiency of wireless communication networks is predominantly determined by the Power amplifier (PA) stage. Low efficiency generally translates into increased running costs for base stations and reduced battery life for mobile handsets. In narrow band systems it has been possible to utilize “traditional” modes of operation, Class B, Class F, etc, to realize high efficiency PA’s. However, these modes require the output matching circuit to present short or open terminations to the relevant harmonics. This termination requirement presents a major circuit design challenge if the application requires wideband power amplifiers. The development of emerging 4G (Fourth Generation) multi-purpose wireless communication networks, such as LTE (Long Term Evolution) that provide higher data-rates (downlink peak rates of at least 100Mbit/s and uplink of at least 50Mbit/s) motivates the microwave community to improve PA performance also in terms of bandwidth. Hence, these new communication systems where increasing bandwidth is very important necessitate a new approach to design high efficiency power amplifiers going beyond the “traditional” modes of operation.

In the “traditional” modes of operation, described in the Snider’s paper [1], utilized basic waveform building blocks. By avoiding the simultaneous presence of harmonic voltage and current components in their

selection high efficiency results. Since, the resulting voltage and current components are harmonically rich, this necessitates the need for harmonic short or open circuit terminations in their practical realization.

On going research activity [2-6] both theoretically and experimentally has indicated that there are alternative modes of operation that can also provide for high efficiency that do allow for the simultaneous, same frequency, presence of harmonic voltage and current components, i.e. Class E [5,6], Class J [3]. The formulation of the “Continuous” modes provides a mathematical framework to describe sets of fundamental/harmonic impedance reactive loci that support specific examples of this mixed mode of operation; delivering the same power and efficiency as the traditional modes on which they are based.

## II. THEORETICAL ANALYSIS

The power and efficiency performance of Power Amplifiers is mathematically determined by analyzing the dynamic, time varying, voltage  $V(t)$  and current  $I(t)$  waveforms that are present on the Power Amplifiers Transistor Terminals.

$$V(t) = f(t) \text{ and } I(t) = g(t) \quad (1)$$

Class A, the reference mode of operation, with a theoretical maximum efficiency of 50%, is defined as both the voltage and current waveforms being sinusoidal.

$$\begin{aligned} V(t) &= f(t) = V_{DC}(1 - \sin(\omega t)) \\ I(t) &= g(t) = \frac{I_{\max}}{2}(1 + \sin(\omega t)) \end{aligned} \quad (2)$$

In this case the waveforms contain no harmonic components. Efficiency performance can be improved via the introduction of harmonic components to modify the shape of the current and/or voltage waveforms. In the work of Snider [1] the current waveform is modified to a half rectified sinusoidal waveform;

$$I(t) = g(t) = \frac{I_{\max}}{2} \left( \frac{2}{\pi} + \sin(\omega t) - \frac{4}{\pi} \sum_{r=1}^{\infty} \frac{\cos(2r\omega t)}{4r^2 - 1} \right) \quad (3)$$

The half rectified sinusoidal current waveform, which contains even harmonics, has the same fundamental component as the reference sinusoidal waveform but a reduced ( $2/\pi$ ) DC level. This is what is exploited in Class B operation to provide for an increase in efficiency of ( $\pi/2$ ) compared to Class A; theoretical maximum 78.5%. The Class B voltage waveform is unchanged, sinusoidal; hence for the transistor to achieve this mode of operation the even harmonic current components must be terminated by short circuits.

In class F the voltage wave is modified to become a square waveform.

$$V(t) = f(t) = V_{DC} \left( 1 - \frac{4}{\pi} \sum_{r=1}^{\infty} \frac{\sin((2r-1)\omega t)}{2r-1} \right) \quad (4)$$

The square waveform, which has odd harmonics, has an increased ( $4/\pi$ ) fundamental component as the reference sinusoidal waveform with the same DC level. Thus in Class F the efficiency increases by an additional factor ( $4/\pi$ ); theoretical maximum 100%. Note, for the transistor to achieve this mode of operation in addition to the class B short circuit termination condition on the even harmonics current components the odd harmonic voltage components must be terminated by open circuits.

Targeting these modes has, until recently, dominated the high efficient PA amplifier design strategy. However, the short and open circuits harmonic terminations has limited their consideration and suitability for wideband applications.

The recently introduced “continuous” mode concept overcomes this bandwidth concern [7]. It provides for alternative impedance terminating solutions that can provide for the same output power and efficiency performance as their reference traditional mode. These impedance solutions are formulated by multiplication of either the current or voltage reference waveform by the following operator;

$$h(t) = (1 - \alpha \cos(\beta \omega t)) \quad -1 \leq \alpha \leq 1 \quad (5)$$

This action will generate a new family of waveforms all having the same DC level and the same fundamental component as the original reference waveform. Hence, a new continuous set of solutions with the same RF output power and efficiency can be identified. A characteristic feature of these new solutions is that they require, or support, a reactive fundamental component and reactive harmonic terminations; hence they are more compatible

with broadband circuit behavior than the original reference waveform that necessitates a real fundamental impedance coupled with harmonic short and/or open circuit terminations.

#### A. Continuous Class B

Lets consider the utilization of the operator introduced in equation (5) for  $\beta=1$  on the Class B voltage waveform. In this case the following set of voltage waveforms solutions are defined when  $\alpha$  is varied from -1 to 1;

$$\begin{aligned} V(t) &= f(t)h(t) \\ V(t) &= V_{DC}(1 - \sin(\omega t))(1 - \alpha \cos(\omega t)) \\ V(t) &= V_{DC}\left(1 - \sin(\omega t) - \alpha \cos(\omega t) + \frac{\alpha}{2} \sin(2\omega t)\right) \end{aligned} \quad (6)$$

Note, all these waveforms have the same DC level and the real fundamental component; hence a theoretical efficiency of 78.5%. The corresponding normalized  $V(t)/V_{DC}$  voltage waveforms are shown graphically in figure 1. They highlight that in order to support these modes the transistor must be able to support an increased maximum voltage swing. This, for example, is not an issue for GaN device technology.

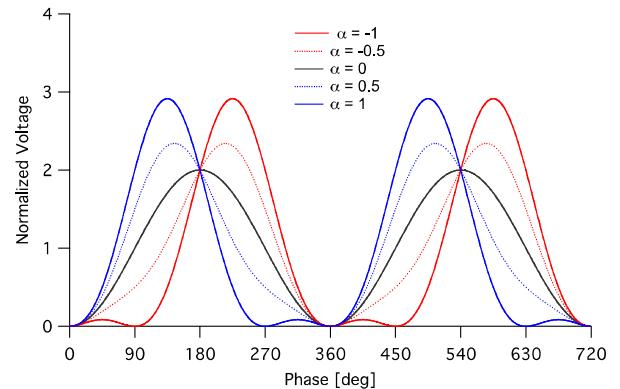


Fig. 1. Family of normalized voltage waveform associated with the continuous class B mode of operation described by equation (6).

Another important observation is that the waveforms computed for  $|\alpha|=1$  are those defined in the Class J mode of operation introduced by Cripps [3]. The continuous formulation identifies that Class B and Class J are alternative solutions of the same mode of operation; hence explaining why they have the same theoretical efficiency and output power prediction.

The corresponding coupled fundamental and second harmonic load impedance loci as a function of  $\alpha$  are given as follows;

$$\begin{aligned} Z_1 &= \frac{2V_{DC}}{I_{\max}}(1+j\alpha) = R_{opt}(1+j\alpha) \\ Z_2 &= \frac{2V_{DC}3\pi}{8I_{\max}}(-j\alpha) = R_{opt}\frac{3\pi}{8}(-j\alpha) \end{aligned} \quad (7)$$

Key feature to note is that these impedances are defined at the transistor current generator reference plane, hence are frequency independent. Wideband operation requires the design of an output matching circuit that can track the coupled fundamental/second harmonic reactive impedance variation described by considering  $\alpha$  varying as a function of frequency. Alternatively the current waveform rather than voltage waveform could be operated on by  $h(t)$  giving another continuous mode solution, defined by loci in the admittance rather than impedance plan in this case.

### B. Continuous Class F

Lets consider the utilization of the operator introduced in equation (5) for  $\beta=1$  on the Class F voltage waveform. In this case the following set of voltage waveforms solutions are defined;

$$\begin{aligned} V(t) &= f(t)h(t) \\ V(t) &= V_{DC} \left( 1 - \frac{4}{\pi} \sum_{r=1}^{\infty} \frac{\sin((2r-1)\omega t)}{2r-1} \right) (1 - \alpha \cos(\omega t)) \quad (8) \\ V(t) &= V_{DC} \left( 1 - \alpha \cos(\omega t) - \frac{4}{\pi} \sum_{r=1}^{\infty} \frac{\sin((2r-1)\omega t)}{2r-1} \right. \\ &\quad \left. + \frac{2\alpha}{\pi} \sum_{r=1}^{\infty} \frac{4r \sin((2r)\omega t)}{4r^2 - 1} \right) \end{aligned}$$

Note, again all these waveforms have the same DC level and the real fundamental component; hence theoretical efficiency of 100%. The corresponding normalized  $V(t)/V_{DC}$  voltage waveforms are shown graphically in figure 2. They highlight that again in order to support these modes the transistor must be able to support an increased maximum voltage swing, approaching 4 times the DC rail voltage. This most likely is not an issue for GaN device technology.

The corresponding fundamental and harmonic load impedance requirement are given as follows;

$$\begin{aligned} Z_1 &= \frac{2V_{DC}}{I_{\max}} \left( \frac{4}{\pi} + j\alpha \right) = R_{opt} \frac{4}{\pi} \left( 1 + j \frac{\pi}{4} \alpha \right) \\ Z_{2r} &= \frac{2V_{DC}}{I_{\max}} (-j2\alpha r) = R_{opt} \frac{4}{\pi} \left( -j \frac{\pi}{2} \alpha r \right) \\ Z_{2r=1} &= \infty \end{aligned} \quad (7)$$

In this case, while the even harmonic open circuit condition remains unchanged fundamental impedance loci with a variable reactive component can be supported provided the even harmonics are appropriately reactively terminated. Wideband operation requires the design of a more complex output matching circuit that can track the coupled fundamental/even harmonic reactive impedance variation described by considering  $\alpha$  varying as a function of frequency. It is envisaged that this can only be achieved over a finite bandwidth. Rather than using the infinite bandwidth square wave, generating a reference waveform based on including only the third harmonic will provide a continuous set of waveforms having a maximum theoretical efficiency to 90.6% [13]. Again, the current waveform rather than voltage waveform could be operated on by  $h(t)$  giving another continuous mode solution, again defined by loci in the admittance rather than impedance plan in this case.

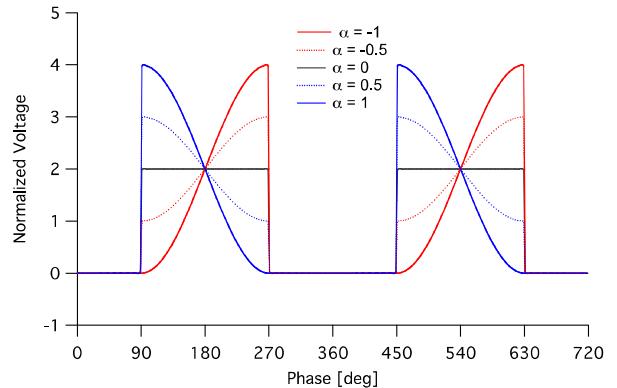


Fig. 2. Family of normalized voltage waveform associated with the continuous class F mode of operation described by equation (8).

### III. EXPERIMENTAL VALIDATION

Continuous Class B and band limited Class F operation have been experimentally verified by performing RF I-V waveform measurements [8] while varying the fundamental and second harmonic load along the computed coupled fundamental/second harmonic variable reactive impedance loci [7, 9-13].

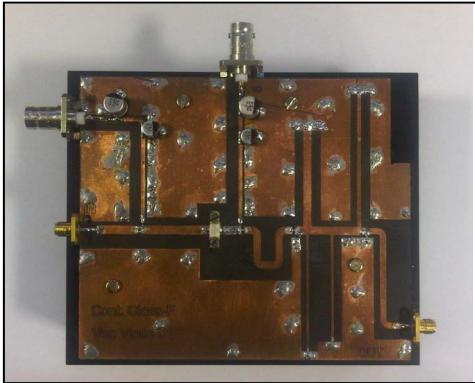


Fig. 3. Prototype wideband high efficiency PA using a GaN 10 W transistor based on the continuous Class-F mode of operation [13].

#### IV. PA PROTOTYPING

Using these experimentally determined coupled fundamental/second harmonic impedance loci output matching circuits have been designed to track these over bandwidth's approach one octave. These resulting realized PA prototypes have demonstrated high efficiency operation over a very wide bandwidth [9, 12, 13]. For example, the prototype PA shown in figure 3 exploited the continuous Class F solution to demonstrate efficiencies of greater than 75% over a frequency range from 0.55 GHz to 0.9 GHz [13].

#### V. SUMMARY

The developed formulation of the “continuous” modes provides a mathematical framework for extending the fundamental/harmonic impedance space over which the power and efficiency performance of the traditional PA modes, i.e/ Class B, Class F, etc. can be maintained. Designing output matching circuits that track these fundamental/second harmonic reactive impedance loci over bandwidths approach an octave has proved feasible enabling the realization of wideband high efficiency prototype PA's.

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